WEST Search History

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DATE: Monday, September 26, 2005

Hide?	<u>Set</u> Name	Query	<u>Hit</u> Count		
DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ					
	L17	L16 and @pd > 20050307	0		
	L16	('20020087924' '4964065') [ABPN1,NRPN,PN,TBAN,WKU]	4		
	L15	L9 and automat\$7 with(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7	55		
	L14	L9 and automat\$7 with(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7 same steady near state	1		
	L13	L9 and automat\$7 same(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) and transmi\$7 same rece\$7 same steady near state and (loopback or loop near back)	1		
	L12	L9 and automat\$7 same(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7 same steady near state and (loopback or loop near back)	1		
	L11	('20020087924') [ABPN1,NRPN,PN,TBAN,WKU]	2		
	L10	L9 and automat\$7 same(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7 same steady near state	1		
	L9	L8 and automat\$7 same(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port same (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7	128		
	L8	L7 and automat\$7 same(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port and (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7	786		
	L7	(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port and (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same transmi\$7 same rece\$7	6918		

L6	automat\$7 same (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7 or measur\$6) same port and (paramet\$4 or ('dc' or 'ac' or (direct or alternating) near (current or voltage or potential))) same (loop\$1back or line with (transmi\$7 or rece\$7)) same algorithm\$4 with (sequen\$6 or vector or pattern)	0
L5	automat\$7 same (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same port and (paramet\$4) same (loop\$1back or line with (transmi\$7 or rece\$7)) same algorithm\$4 with (sequen\$6 or vector or pattern)	0
L4	serial same (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same port and (paramet\$4) same (loop\$1back or line with (transmi\$7 or rece\$7)) same algorithm\$4 with (sequen\$6 or vector or pattern)	0
L3	serial same (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same port same (paramet\$4) same (loop\$1back or line with (transmi\$7 or rece\$7)) same algorithm\$4 with (sequen\$6 or vector or pattern)	0
L2	serial same (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same port same paramet\$4 same loop\$1back same algorithm\$4 near (sequen\$6 or vector or pattern)	0
L1	serial same test\$3 same port same paramet\$4 same loop\$1back same	0

END OF SEARCH HISTORY